

AF
JFW

S/N 09/893,527

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Shmuel Wimer

Examiner: Dwin M Craig

Serial No.: 09/893,527

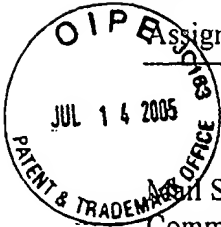
Group Art Unit: 2123

Filed: June 29, 2001

Docket No.: 80107.072US1

Title: ARRANGEMENTS FOR AUTOMATIC RE-LEGGING OF TRANSISTORS

Assignee: Intel Corporation



AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116

Attn: Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This responds to the Final Office Action mailed on June 30, 2005. Please amend the above-identified patent application as follows.

Dmc
9-6-2005
Please enter